Appl. No. 10/645,366 Amdt. Dated 07/25/2005

Reply to Office Action of 04/26/2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.

10/645,366

Confirmation No. 7488

Applicant

Ruban Kanapathippillai, et al.

Filed

: 08/21/2003

Title

: METHODS FOR POWER REDUCTION IN A DIGITAL SIGNAL

PROCESSOR INTEGRATED CIRCUIT (As Amended)

TC/A.U.

: 2111

Examiner

: Kenneth S. Kim

Docket No. : 42P14037D

Customer No. : 8791

RESPONSE TO OFFICE ACTION

Commissioner for Patents PO Box 1450 Alexandria VA 22313-1450

Dear Sir:

In response to the Office Action mailed on 04/26/2005, please enter the following amendments and remarks in the aboveidentified patent application as follows below:

Specification Amendments begin on page 2.

Claim Amendments begin on page 3.

Remarks begin on page 10.

Conclusion with signature is on page 22.

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